

## **REMARKS**

Claims 1-26 are pending. Claims 1-26 are rejected in the present Office Action. Applicant respectfully submits that the rejection of claims 1-26 under 35 U.S.C. §112 and §102 should be removed for the reasons set forth hereinbelow.

### **Claim Rejections – 35 U.S.C. § 112**

Claims 12 and 25 are rejected under 35 U.S.C. §112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Claims 12 and 25 have been amended to change the reference to the “second” clock to the “first” clock signal.

### **Claim Rejections – 35 U.S.C. § 102**

Claims 1-26 are rejected under 35 U.S.C. §102(b), as being anticipated by U.S. Patent No. 6,473,607 to Shohara et al. (hereinafter “Shohara”).

Independent claims 1 and 14 have been amended to recite that the power management logic is operable to calibrate the frequency of the clock generator while the wireless communication system is operating in the second power mode. This feature is discussed in more detail in paragraph 35 of the specification. Specifically, in the present invention, the power management logic 150 is operable to calibrate the frequency of the low power oscillator 158 while the wireless communication system is operating, thereby ensuring the system can maintain high accuracy timers while operating in the low power mode.

Shohara does not teach a system that is operable to calibrate the frequency of a low power oscillator while the system is operating. Instead, Shohara teaches a system wherein the low power clock is adjusted after it exits the power down mode. Specifically, Shohara teaches that the number of clock cycles counted by the timer when the wireless communication system is operating in the second (low) power mode is converted to an equivalent number of clock cycles that would have been generated by the first clock by using an adjustment factor based on the number of cycles the first clock will generate during a single cycle of the second clock. See, for example, the discussion in column 13, lines 41-60 of Shohara stating that the “sleep counter accumulates a fraction of the sleep increment value in the sleep increment register since the value is an

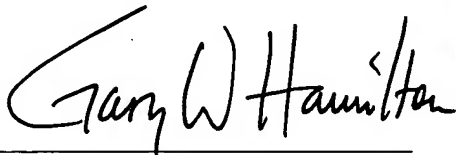
estimated number of frames per sleep clock cycle, where the fraction value is the frame count.” Applicants respectfully submit that the cited portion of Shohara does not meet the calibration limitation recited in independent claims 1 and 14, as amended.

In view of the foregoing, it is respectfully submitted that independent claims 1 and 14 as amended distinguish over the art of record and, therefore, are allowable. Furthermore, it is respectfully submitted that dependent claims 2-13 and 15-26 are allowable as being dependent from allowable base claims.

### CONCLUSION

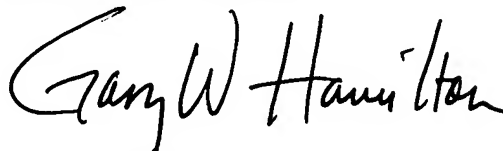
In view of the amendments and remarks set forth herein, Applicant respectfully submits that all pending claims are in condition for allowance. Accordingly, Applicant requests that a Notice of Allowance be issued. Nonetheless, should any issues remain that might be subject to resolution through a telephone interview, the Examiner is requested to telephone the undersigned at 512-338-9100.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450, on May 19, 2006.



Attorney for Applicant(s)

Respectfully submitted,



Gary W. Hamilton  
Attorney for Applicants  
Reg. No. 31,834